500.33021CX5

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: NINOMIYA, et al

Serial No.: Not yet assigned

Filed: August 22, 2001

For: STORAGE SYSTEM

Group: 2812

Examiner: S. Whitmore

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §1.97 & 1.98

Assistant Commissioner for Patents Washington, D.C. 20231 August 22, 2001

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO-1449 which list documents cited in parent applications Serial No. 09/440,285, filed November 15, 1999, which is a continuation of application Serial No. 09/013,039, filed January 26, 1998, now U.S. Patent No. 6,012,119; which is a continuation of Serial No. 08/819,625, filed March 17, 1997,

now U.S. Patent No. 5,819,054; which is a continuation of Serial No. 08/267,013, filed June 21, 1994, now abandoned and copies of references recently uncovered by Applicants.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

charge any shortage in the due fees in connection with the filing of this paper, extension of time fees, to the deposit account of Antonelli, Deposit Terry, Stout Kraus Account No. (500.33021CX5) please credit any excess fees to such deposit account.

Respectfully submitted,

Carl I. Brundidge

CIB/jdc

(703) 312-6600

Registration No. 29,621 ANTONELLI, TERRY, STOUT & KRAUS, LLP

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	U.S. PATENT DOCUMENTS													
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	5	1	4	8	4	3	2	9/92	Gordon et al	395	182.05			
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Adams et al, "Database Computer based on a multi-Microprocessor System", December 1986, NTIS.

"Using Dual and Mappable Spare Bus", IBM Technical Disclosure Bulletin, Feb. 1994, pp. 59-64.

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